



U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
AC TM ³ SCANNED Q.A. YR	

APPLICATION NO. 09/655595	CONT/PRIOR	CLASS <u>257</u> <u>703</u>	SUBCLASS <u>28</u>	ART UNIT <u>2811</u> <u>2123</u>	EXAMINER <u>Stevens, Tera</u>
APPLICANTS William Beausoleil R. Cook Tak-Kwong Ng Helmut Roth					
TITLE High speed software driven emulator comprised of a plurality of emulation processors with improved board-to-board interconnection cable length identification system					

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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				Amount Due	Date Paid
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